

SYLVANIA

SYLVANIA ELECTRIC PRODUCTS INC.

Subsidiary of GENERAL TELEPHONE & ELECTRONICS CORPORATION



Electronic Components Group

Seneca Falls, New York 13148

February 3, 1967

Correction to 35 and 50 MHz Dual J-K Flip Flop Pin Basing

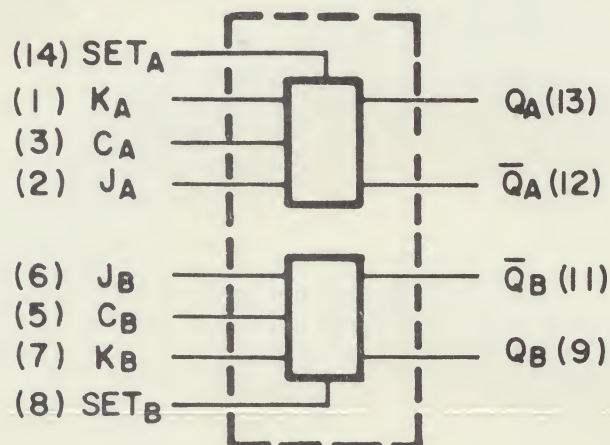
TO: SELECTED INTEGRATED CIRCUIT CUSTOMER MAILING LIST

The attached diagram shows the correct pin basing for the SF100/110 and SF120/130 Dual J-K Flip Flops. This diagram is an addendum to the SF100/110 and SF120/130 New Product Reports and replaces the pin basing diagrams found on these New Product Reports. We are sorry for the inconvenience this oversight may have caused. Your design requirements are of sufficient importance to us, however, to make certain that your attention has been directed to this matter.

J. W. RITTER
Marketing Manager - OEM

JWR:dt

PIN BASING FOR SYLVANIA'S DUAL J-K FLIP-FLOPS

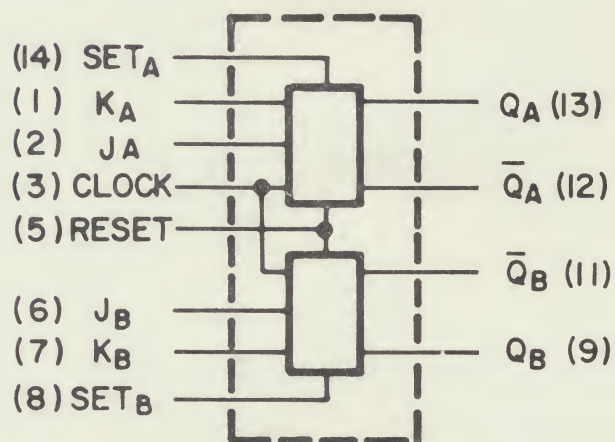


35 MC

SF-100	MP
SF-101	MS
SF-102	IP
SF-103	IS

50MC

SF-120
SF-121
SF-122
SF-123

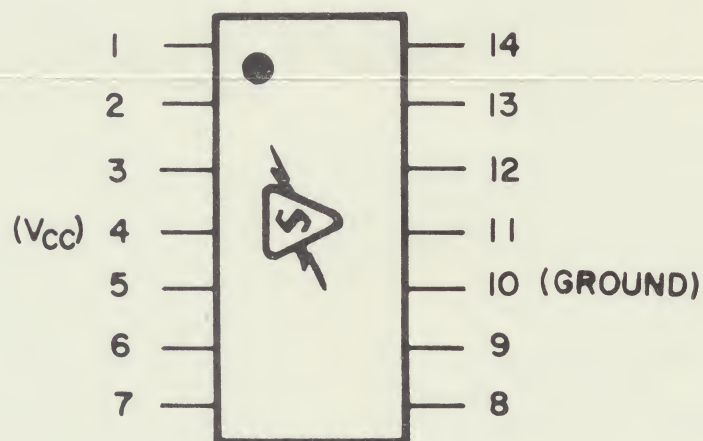


35MC

SF-110	MP
SF-111	MS
SF-112	IP
SF-113	IS

50MC

SF-130
SF-131
SF-132
SF-133



SYLVANIA

SUBSIDIARY OF

GENERAL TELEPHONE & ELECTRONICS

